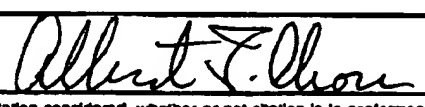

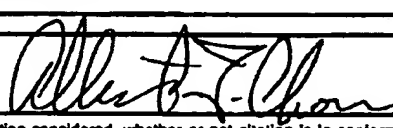

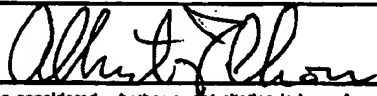

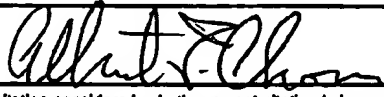
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	BM						
	BN						
OTHER DISCLOSURES (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)							
AC	BA	H. Samavati, H. R. Rategh, T.H. Lee, "A 5-GHz CMOS Wireless LAN Receiver Front End", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, pp. 765-777, May 2000					
AC	BS	J. C. Rudell, J.J. Ou, T. B. Cho, G. Chien, F. Brianti, J. A. Weldon, P. R. Gray, "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, pp.2071-2088, December 1997					
EXAMINER				DATE CONSIDERED			
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OTHER DISCLOSURES (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)							
AC	CR	"Atheros 'Rearchitects' the Radio; 'Radio-on-a-Chip' Shatters Wireless LAN Price/Performance Barrier", Press Release, Atheros Communications, Inc., Sunnyvale, CA, September 18, 2000					
AC	CS	"Next-Generation Wireless LAN in Mainstream CMOS Technology", Brochure, Atheros Communications, Inc., Sunnyvale CA, Sept. 2000					
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	DM						
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AC	DR	Radiata First to Deliver High-Performance, Low-Cost Wireless Networking Solution - Industry's First IEEE-802.11a Chipset Delivers Five Times the Performance of Current Wireless Networking Solutions at Comparable Cost", Press Release, Issued Sep. 15, 2000, Radiata, Inc. San Jose, CA					
AC	DS	Radiata Technology Enables Chip-Level 'Wireless Engine'", Brochure, Issued Sep. 15, 2000, Radiata, Inc. San Jose, CA					
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	EM						
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AC	ER	CMOS chip sets accelerate shift to 5-GHz wireless nets", Patrick Mannion, EETimes, Sep. 18, 2000					
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